GINZBURG, Boris et al.

Serial No.:

10/608,143

Attorney Docket No.: Assignee:

P-5751-US

Intel Corporation

Amendments to the Claims

The following Listing of Claims replaces all prior versions and Listings of Claims in the application:

Listing of Claims:

- (Currently Amended) A method comprising:
 transmitting by a wireless communication device during an awake mode of [[a]]
 said wireless communication device one or more data packets sent for transmission
 during a power save mode of said wireless communication device.
- 2. (Original) The method of claim 1, further comprising buffering said one or more data packets during said power save mode.
- 3. (Original) The method of claim 2, wherein transmitting during an awake mode comprises transmitting said one or more packets in response to a wake-up trigger.
- 4. (Original) The method of claim 3, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.
- 5. (Original) The method of claim 3, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.
- 6. (Original) The method of claim 3, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.
- 7. (Original) The method of claim 2, wherein buffering comprises buffering one or more of said data packets based on a priority criterion.

Applicants: Serial No.: GINZBURG, Boris et al.

10/608,143

Attorney Docket No.:

Assignee:

P-5751-US

Intel Corporation

8. (Original) The method of claim 7, wherein transmitting comprising transmitting said one or more data packets based on said priority criterion.

- 9. (Original) The method of claim 8, wherein said priority criterion relates to the priority of said one or more data packets.
- 10. (Original) The method of claim 1, wherein transmitting during an awake mode comprises transmitting an awake mode signal to indicate a start of said awake mode.
- 11. (Original) The method of claim 1, wherein transmitting during an awake mode comprises transmitting a power save signal to indicate an end of said awake mode.
- 12. (Original) The method of claim 1, comprising disabling a transmitter during said power save mode.
- 13. (Currently Amended) A program storage device having instructions readable by a machine that when executed by the machine result in: transmitting by a wireless communication device during an awake mode of [[a]] said wireless communication device one or more data packets sent for transmission during a power save mode of said wireless communication device.
- 14. (Original) The program storage device of claim 13, wherein said instructions further result in buffering said one or more data packets during said power save mode.

GINZBURG, Boris et al.

Serial No.:

10/608,143

Attorney Docket No.:

Assignee:

P-5751-US Intel Corporation

15. (Original) The program storage device of claim 14, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting said one or more packets in response to a wake-up trigger.

- 16. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.
- 17. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.
- 18. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.
- 19. (Original) The program storage device of claim 14, wherein the instructions that result in buffering comprise instructions that result in buffering one or more of said data packets based on a priority criterion.
- 20. (Original) The program storage device of claim 19, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting said one or more data packets based on said priority criterion.
- 21. (Original) The program storage device of claim 20, wherein said priority criterion relates to the priority of said one or more data packets.
- 22. (Original) The program storage device of claim 13, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting an awake mode signal to indicate a start of said awake mode.

GINZBURG, Boris et al.

Serial No.:

10/608,143

Attorney Docket No.:

Assignee:

P-5751-US Intel Corporation

23. (Original) The program storage device of claim 13, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting a power save signal to indicate an end of said awake mode.

24. (Original) The program storage device of claim 13, wherein the instructions result in disabling a transmitter during said power save mode.

25. (Currently Amended) An apparatus comprising:

a buffer to store one or more data packets during a power save mode of said apparatus; and

a transmitter <u>operatively coupled to said buffer</u>, <u>said transmitter</u> to transmit <u>during</u> an <u>awake mode of said apparatus</u> said one or more data packets <u>stored by said buffer</u> during <u>an awake said power save</u> mode of said apparatus.

- 26. (Original) The apparatus of claim 25, further comprising a processor adapted to transmit an awake signal to indicate a start of said awake mode.
- 27. (Original) The apparatus of claim 26, wherein said processor is further adapted to transmit a power save signal to indicate an end of said awake mode.
- 28. (Original) The apparatus of claim 27, comprising a disabling unit to disable said transmitter during said power save mode.
- 29. (Original) The apparatus of claim 28, wherein said disabling unit is able to enable said transmitter during said power save mode.

GINZBURG, Boris et al.

Serial No.:

10/608,143

Attorney Docket No.: P-5751-US

Assignee:

P-5751-US Intel Corporation

30. (Currently Amended) A wireless communication device comprising:

a buffer to store one or more data packets during a power save mode of said

wireless communication device;

a transmitter operatively coupled to said buffer, said transmitter to transmit during

an awake mode of said wireless communication device said one or more data

packets stored by said buffer during an awake said power save mode of said

wireless communication device; and

an omni-directional antenna operationally coupled to said transmitter.

31. (Original) The wireless communication device of claim 30, further comprising a

processor to produce said one or more data packets.

32. (Original) The wireless communication device of claim 31, wherein said

transmitter is further adapted to transmit an awake mode signal to indicate a start of

said awake mode.

33. (Original) The wireless communication device of claim 31, wherein said

transmitter is further adapted to transmit a power save mode signal to indicate an

end of said awake mode.

34. (Original) The wireless communication device of claim 31, further comprising a

power source and circuitry to connect said transmitter to said power source during

said awake mode.

35. (Previously Presented) The wireless communication device of claim 34, further

comprising circuitry to disconnect said transmitter from said power source during a

power save mode.

6

GINZBURG, Boris et al.

Serial No.:

10/608,143

Attorney Docket No.: P-5751-US

Assignee:

Intel Corporation

36. (Currently Amended) A wireless communication system comprising:

a first wireless communication device adapted to transmit during an awake mode of said first wireless communication device one or more data packets sent for transmission by said first wireless communication device during a power save mode of said first wireless communication device; and

a second wireless device adapted to receive said one or more data packets.

- 37. (Original) The wireless communication system of claim 36, wherein said second wireless device is further adapted to transmit during said awake mode one or more data packets sent for transmission during said power save mode.
- 38. (Original) The wireless communication system of claim 37, wherein said first wireless device is further adapted to transmit an awake mode signal to indicate a start of said awake mode.
- 39. (Previously Presented) The wireless communication system of claim 38, wherein said first wireless device is further adapted to transmit a power save mode signal to indicate an end of said awake mode.